

FIG. 1A

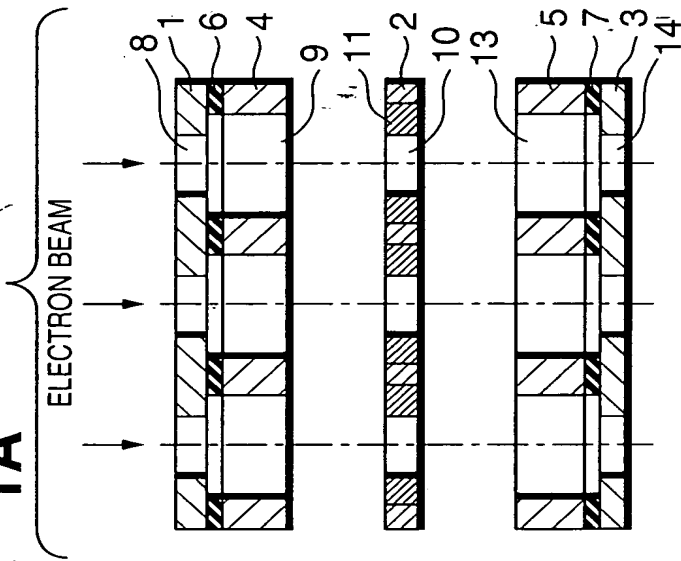


FIG. 1C

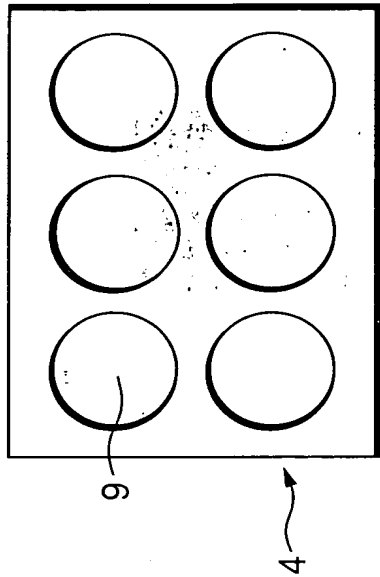


FIG. 1D

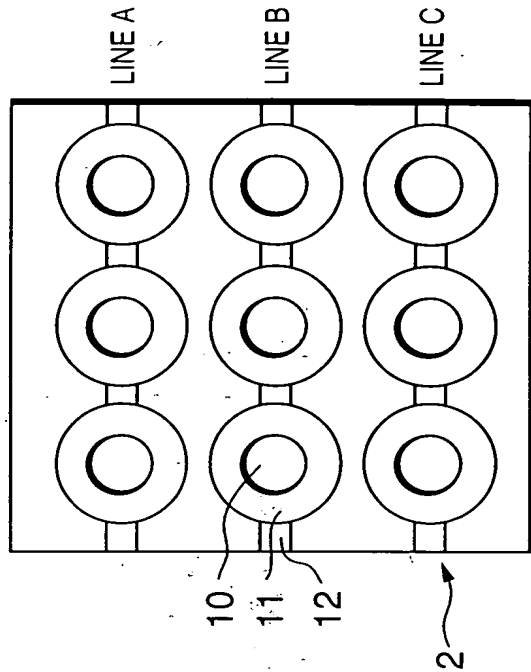
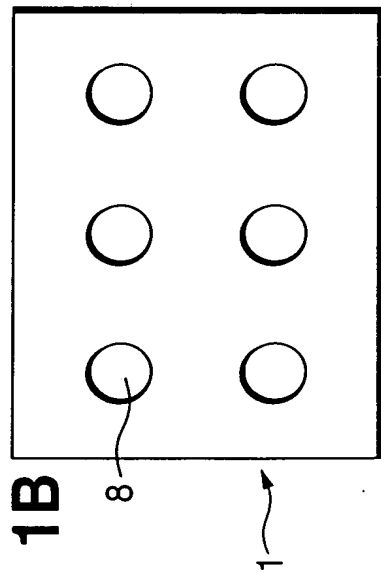


FIG. 1B



Handwritten signature

FIG. 2A

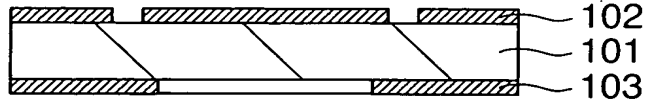


FIG. 2B

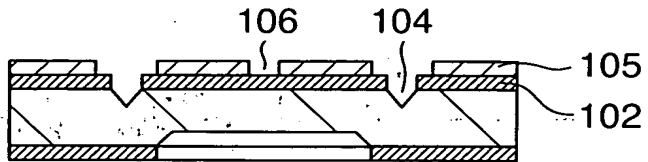


FIG. 2C

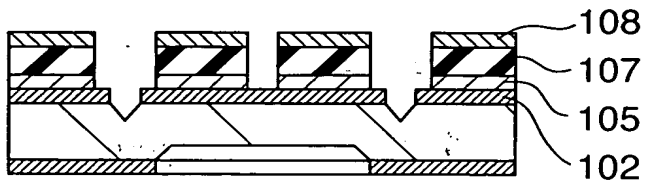


FIG. 2D

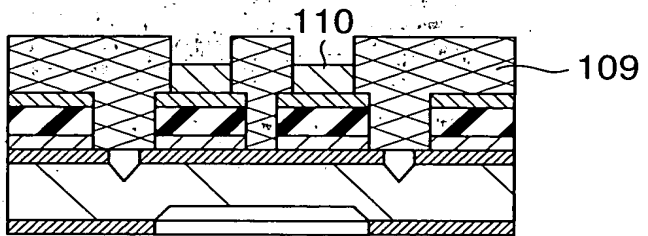


FIG. 2E

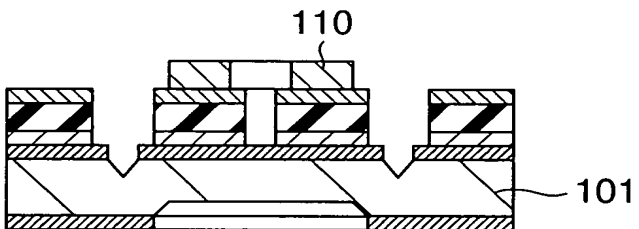
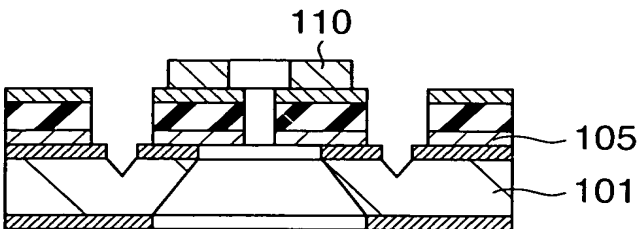
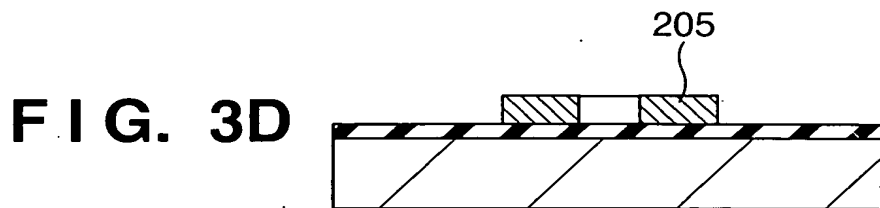
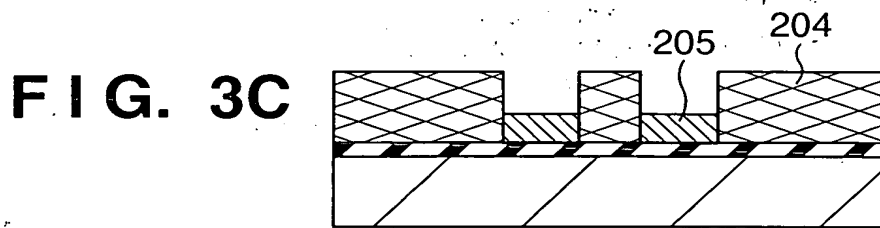
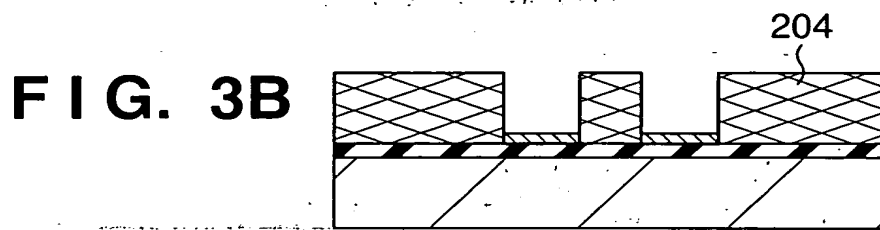
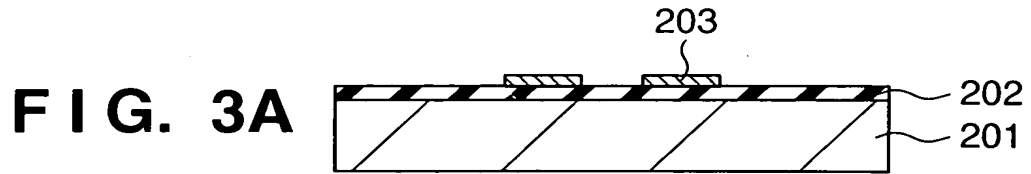


FIG. 2F





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FIG. 4A

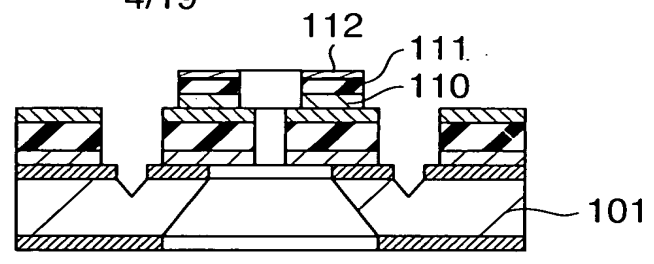


FIG. 4B

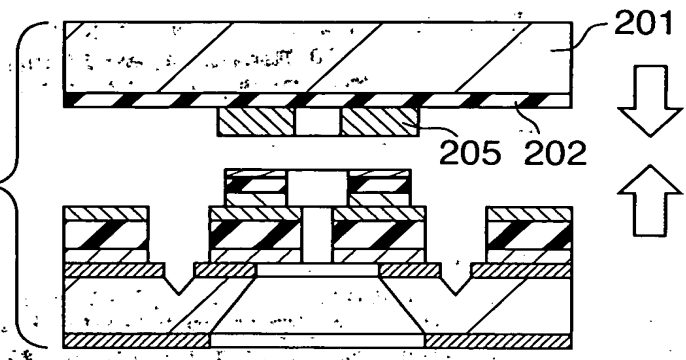


FIG. 4C

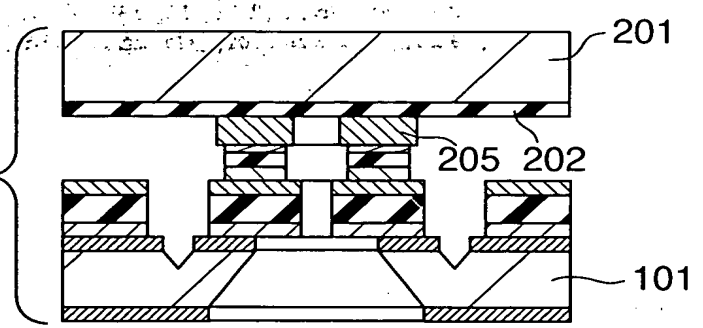


FIG. 4D

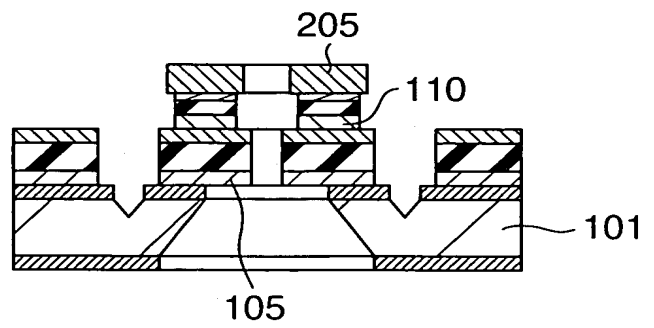
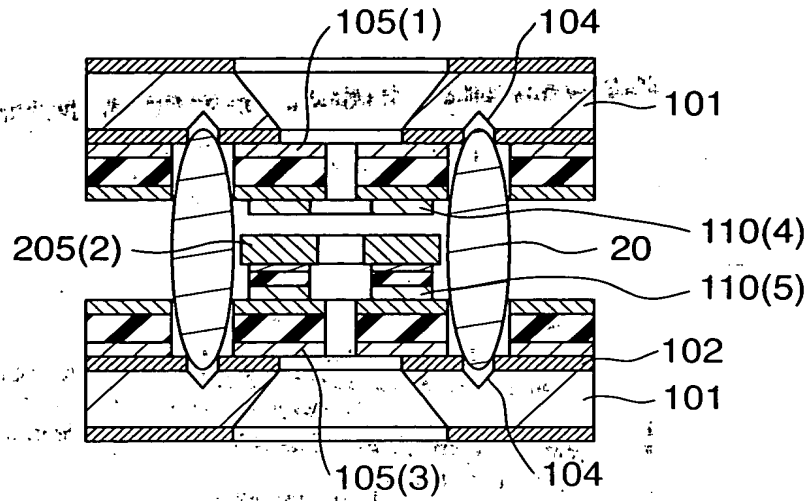
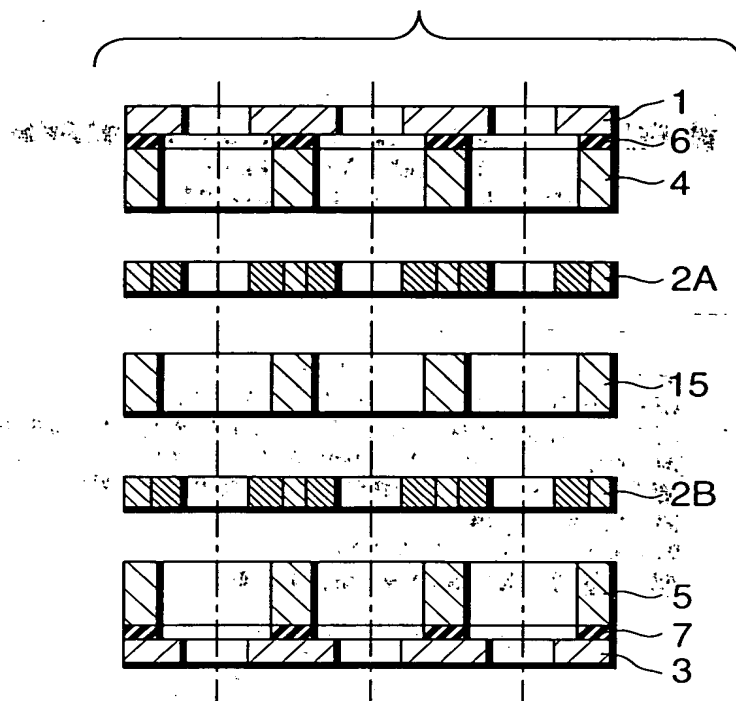


FIG. 5



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FIG. 6



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FIG. 7A

FIG. 7A

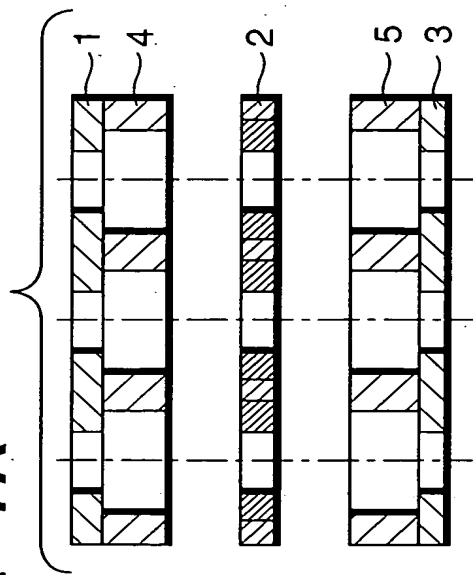
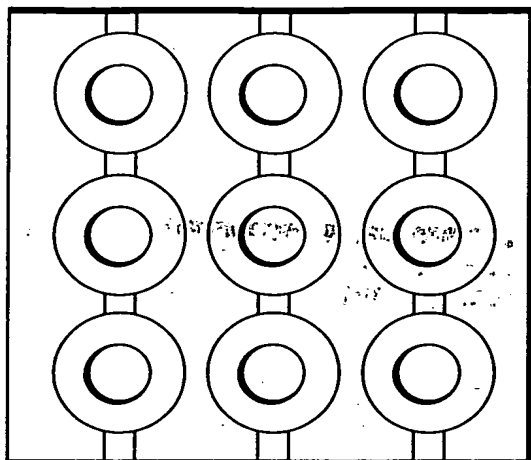
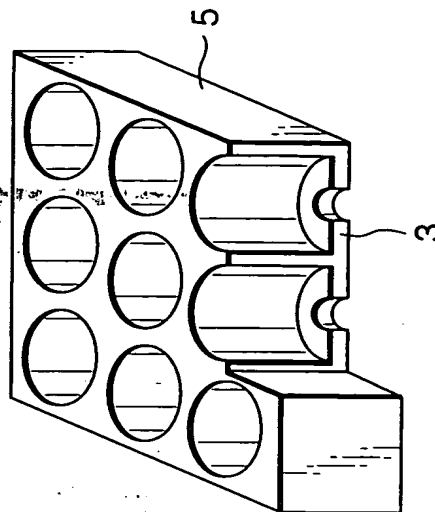


FIG. 7B



2

FIG. 7C



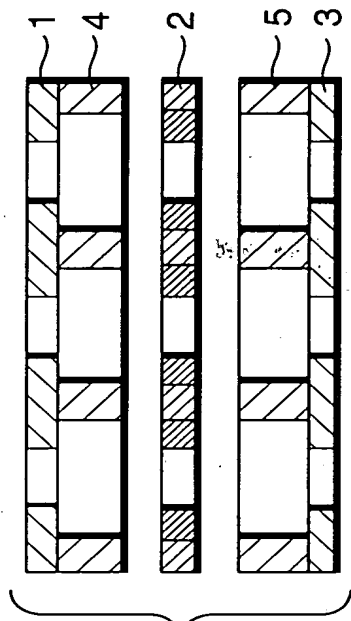


FIG. 8A

FIG. 8B

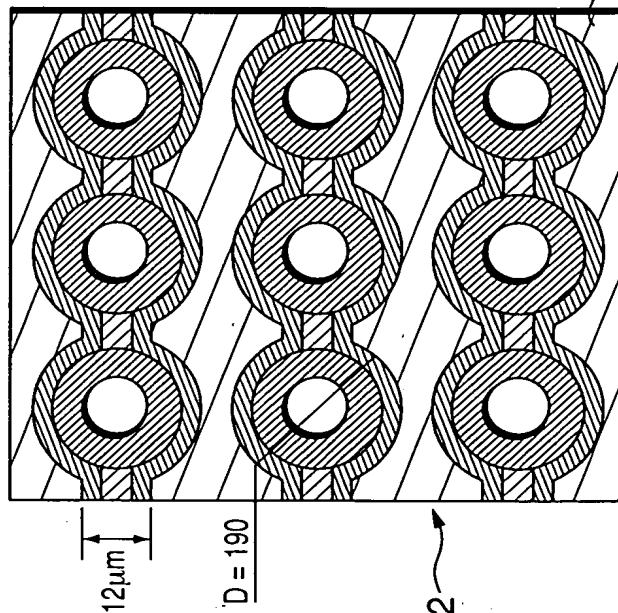


FIG. 8C

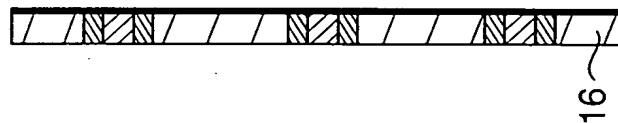


FIG. 8D

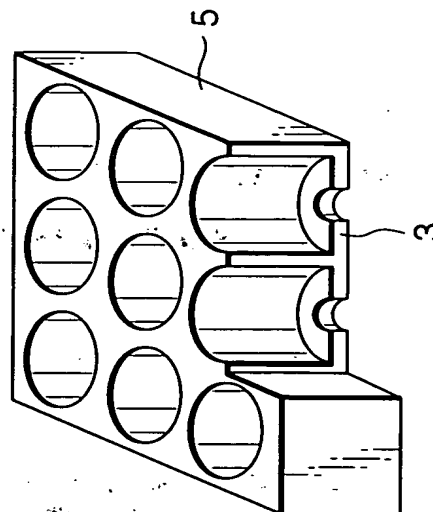


FIG. 9A

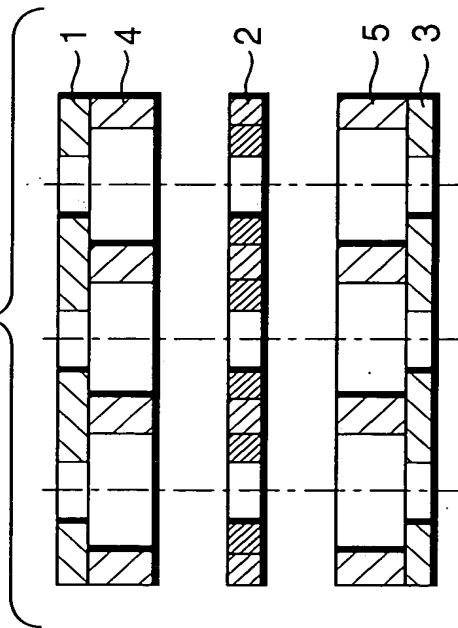


FIG. 9B

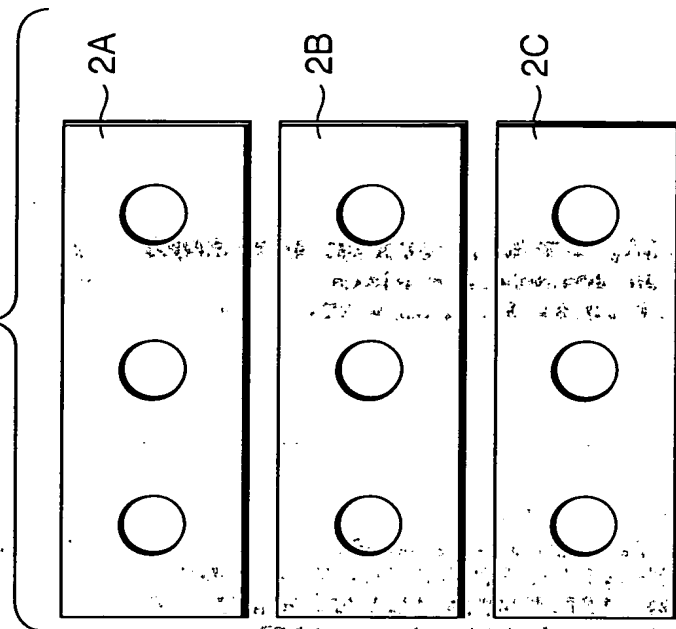


FIG. 9C

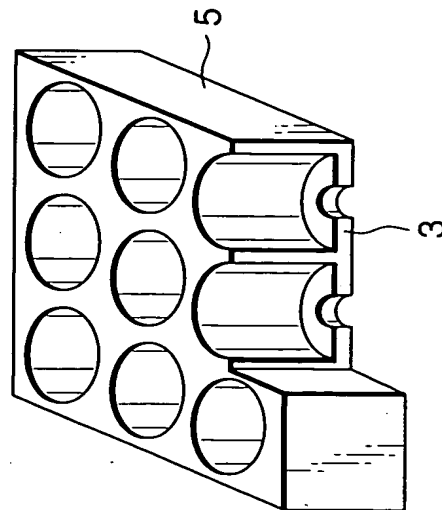
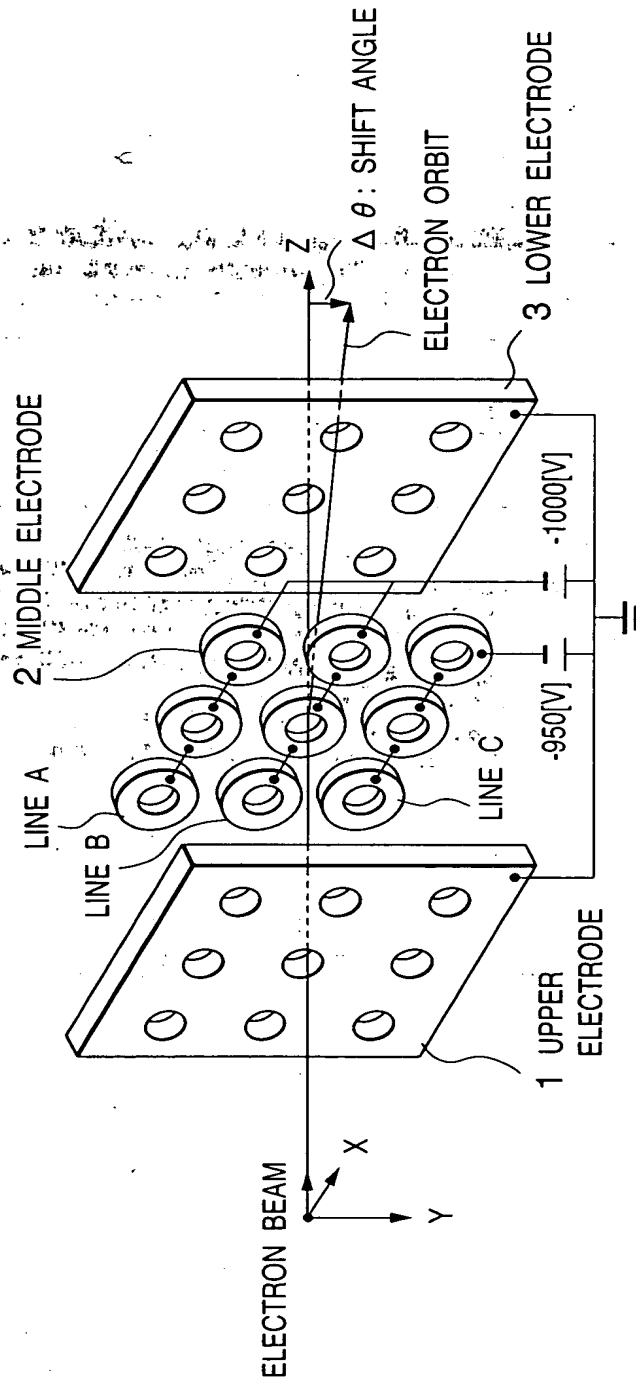


FIG. 10



A cross-sectional view of a semiconductor device. A substrate 2 is shown with a grid of circular elements 11. The elements are arranged in three rows and three columns. The vertical spacing between the rows is labeled D_c . The elements are connected to a biasing circuit consisting of three voltage sources V_1 , V_2 , and V_3 connected in series to ground. The circuit is connected to the elements via vertical lines 12.

The schematic diagram illustrates a semiconductor device. On the left, a cross-sectional view shows a substrate with a series of vertical layers and regions. Labels 15, 12, and 11 point to specific layers or regions. A label 2 points to a top layer. To the right, a circuit diagram shows three voltage sources, V1, V2, and V3, connected in series. A supply voltage V_s is connected to the bottom of the device structure.

FIG. 12A

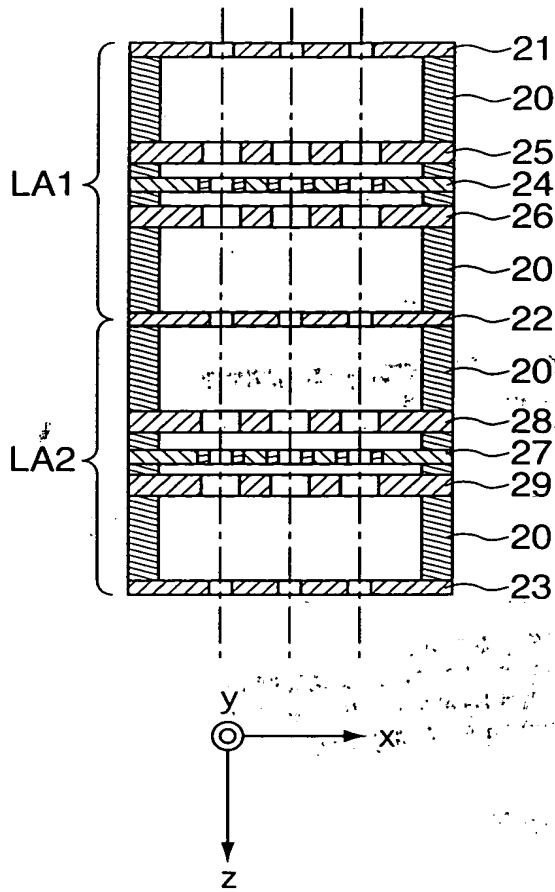


FIG. 12B

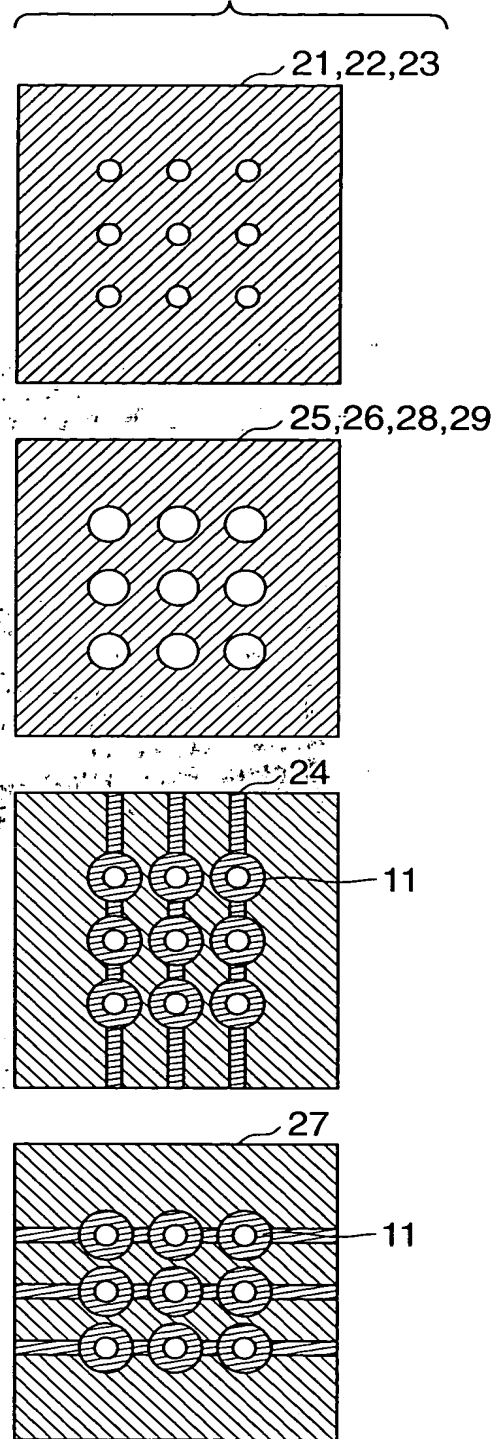
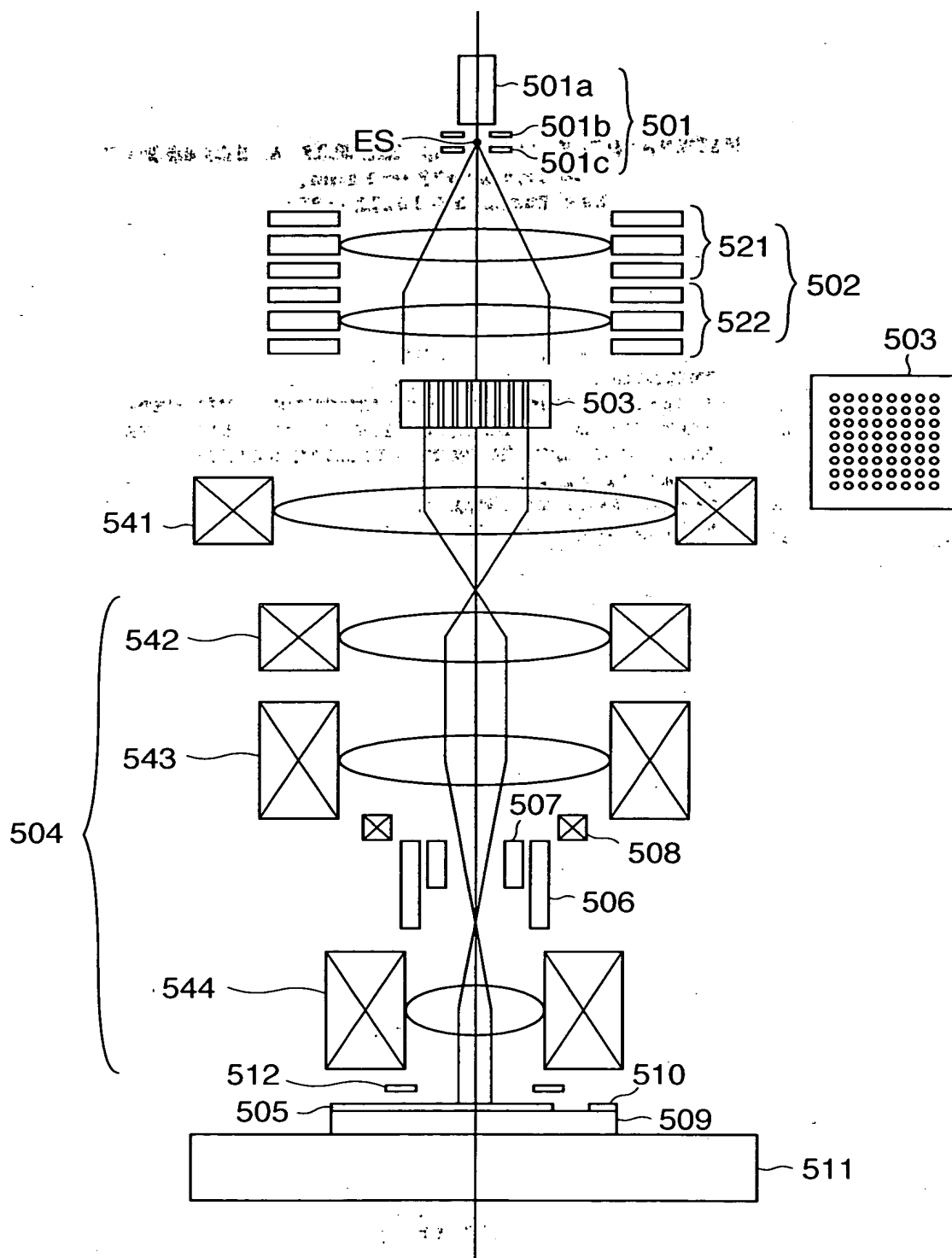


FIG. 12A

FIG. 13



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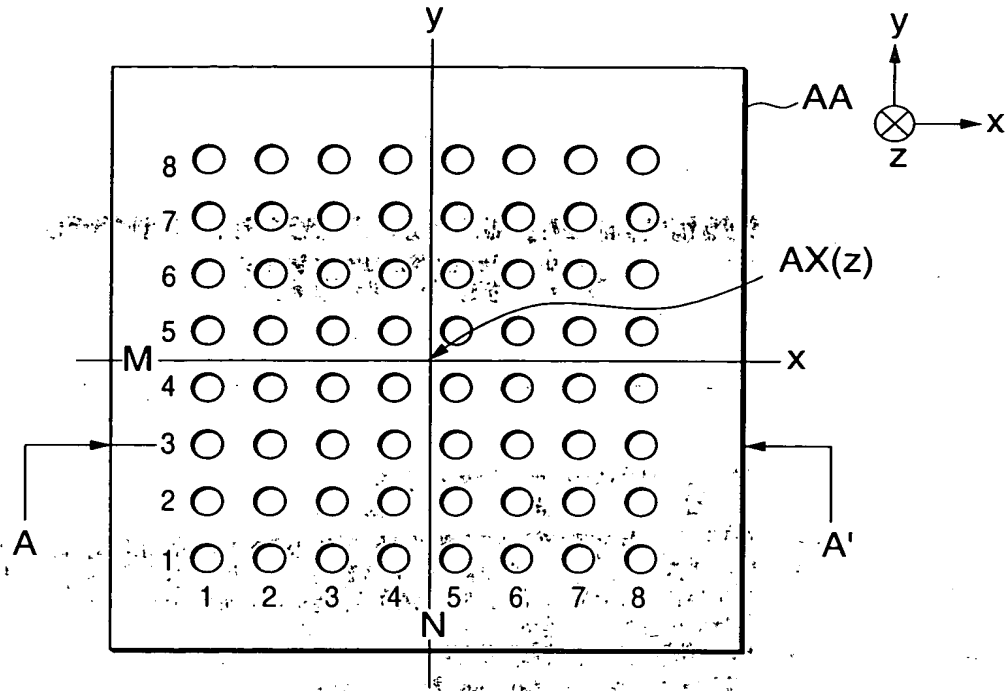
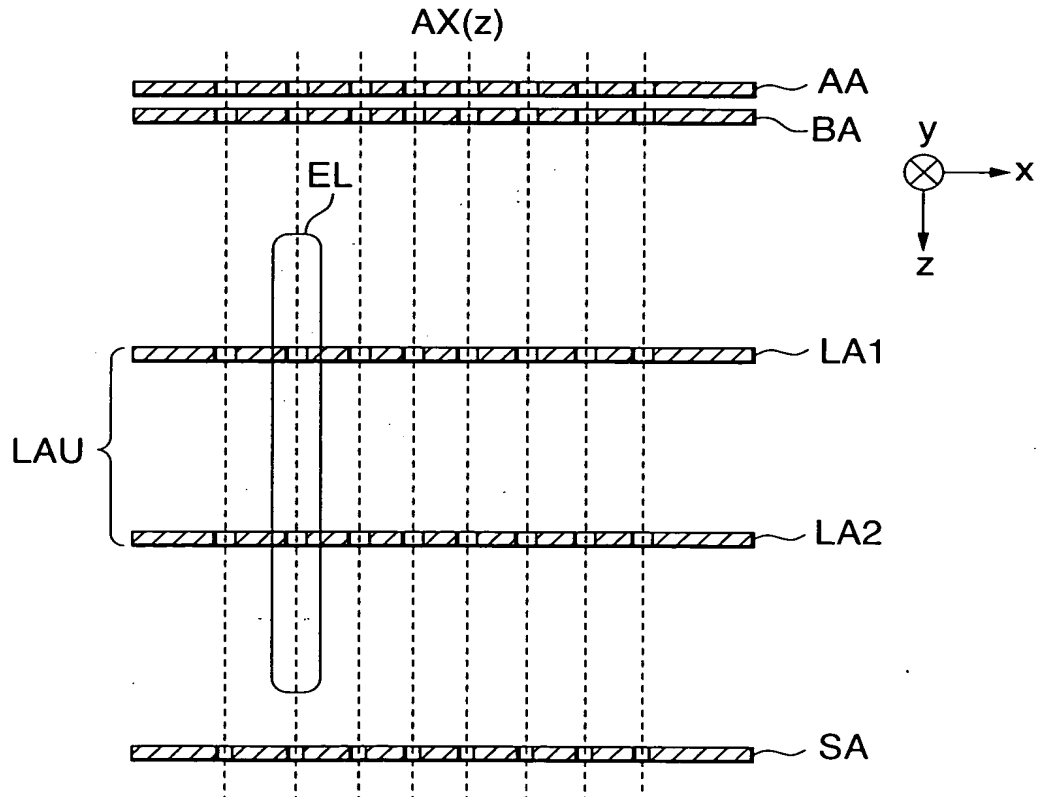
FIG. 14A**FIG. 14B**

FIG. 15

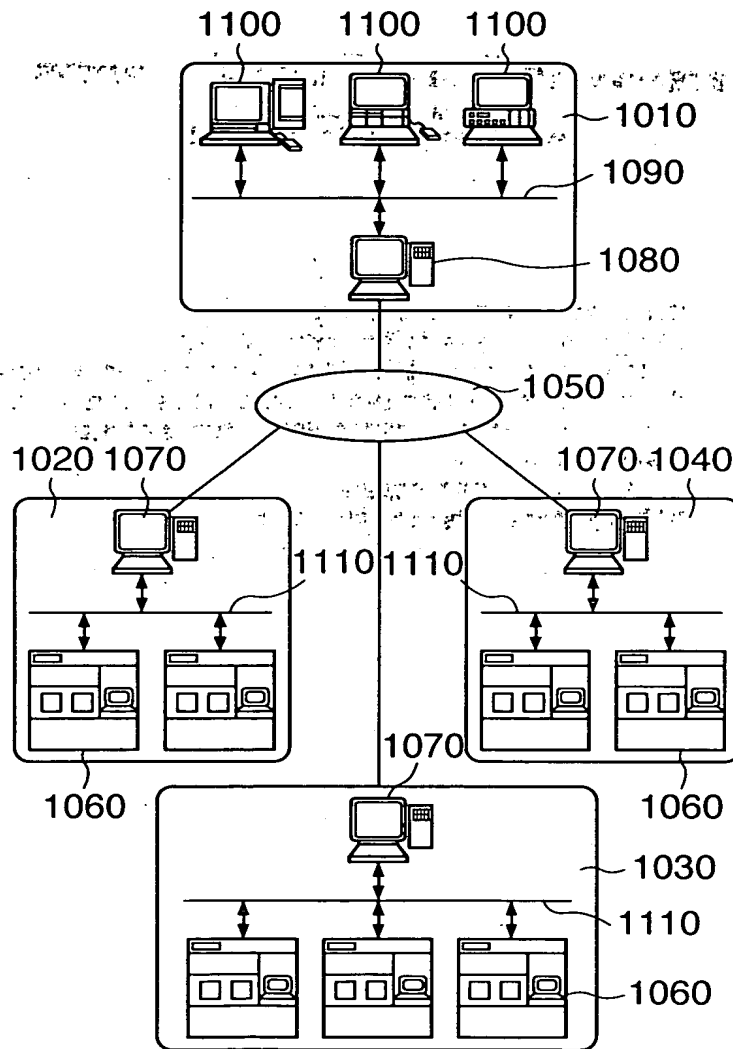


FIG. 15

FIG. 16

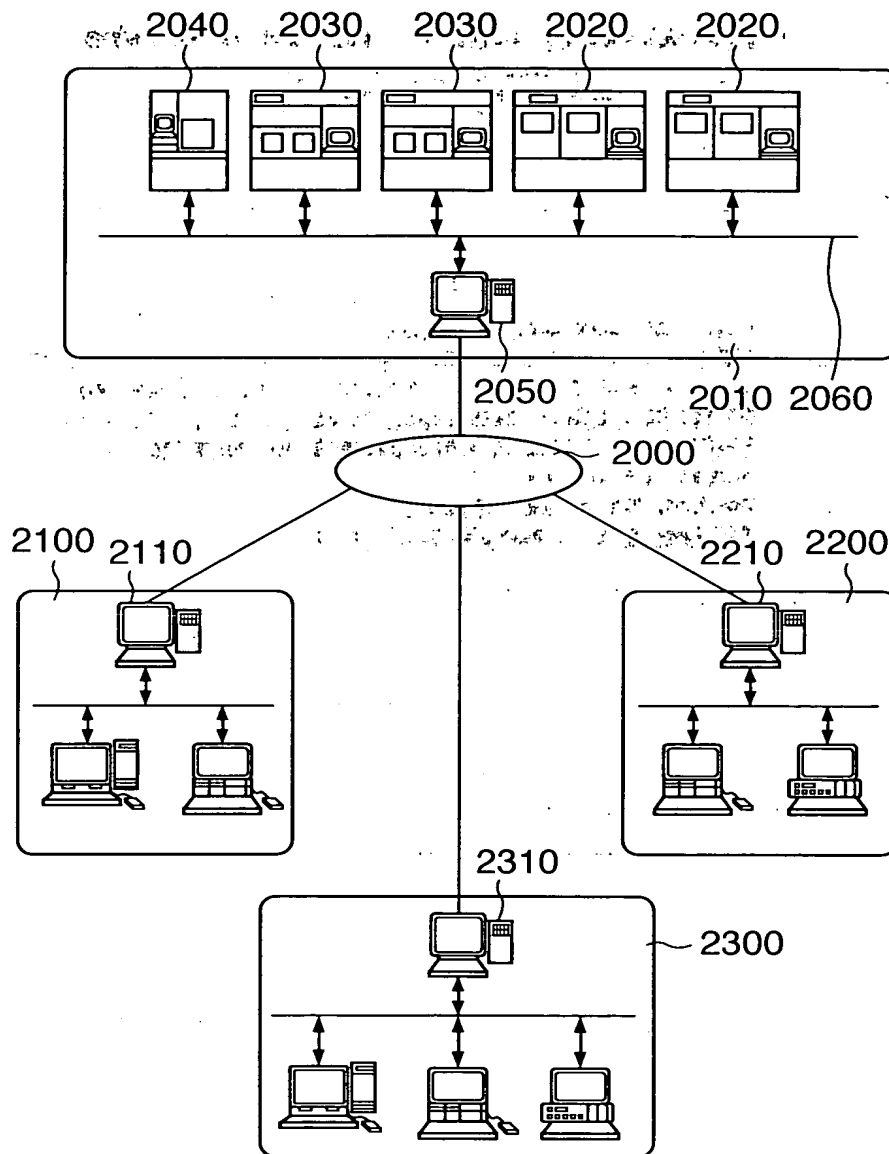


FIG. 17

URL

TROUBLE DB INPUT WINDOW

OCCURRENCE DATE

4040

TYPE OF APPARATUS

4010

SUBJECT

4030

SERIAL NUMBER S/N

4020

DEGREE OF URGENCY

4050

SYMPTOM

LED IS KEPT FLICKERING AFTER POWER-ON

4060

REMEDY

POWER ON AGAIN (PRESS RED BUTTON IN ACTIVATION)

4070

PROGRESS

INTERIM HAS BEEN DONE

4080

SEND

RESET

4100

4110

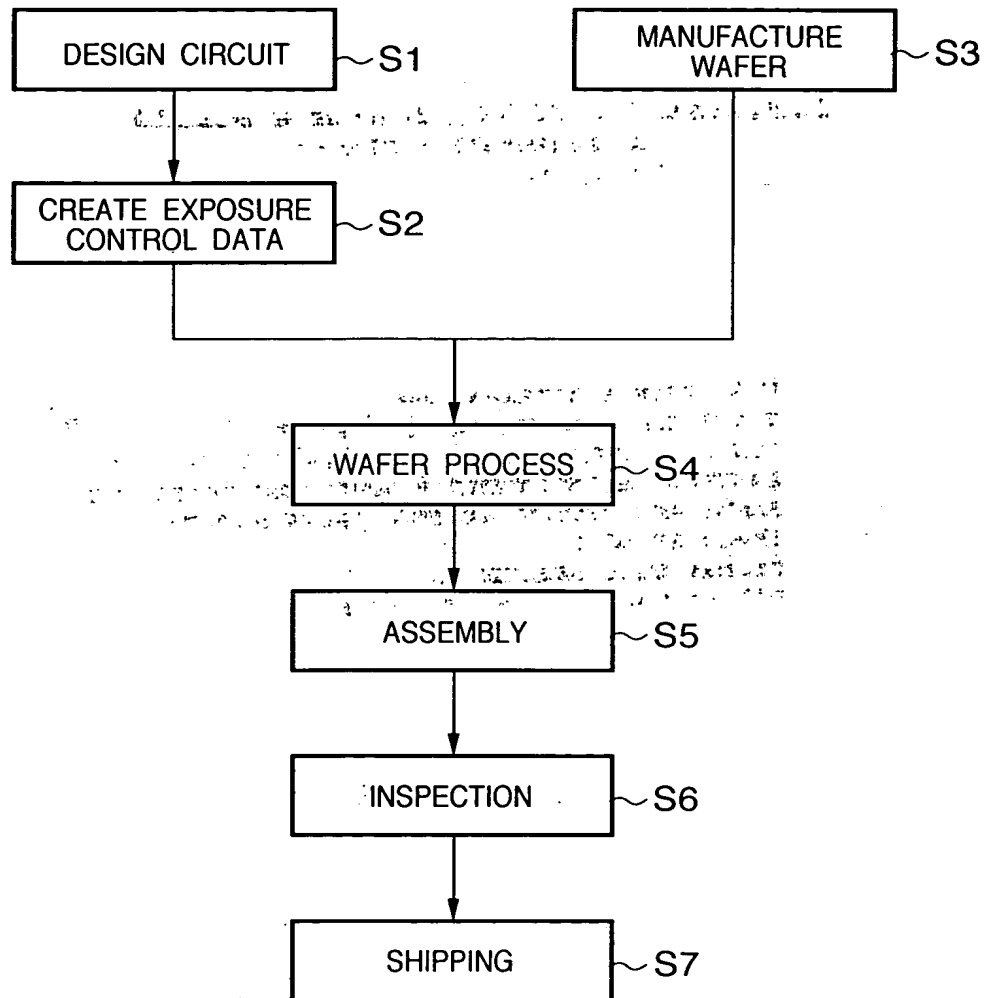
4120

LINK TO RESULT LIST DATABASE

SOFTWARE LIBRARY

OPERATING GUIDE

FIG. 18



FOUO " 42/67860

FIG. 19

